

## AMENDMENTS TO SPECIFICATION

Please amend page 7, lines 2-19, as follows:

The microstrip circuit of a formed low pass filter having the above impedance values is shown in FIG. 3. One end of the microwave circuit at input terminal 1 is perpendicular to one end of the coupled first microstrip circuit  $Z_1$ . One end of the microwave circuit at output terminal 5 is perpendicular to one end of the coupled third microstrip circuit  $Z_3$ . The first microstrip circuit  $Z_1$  is parallel to the third microstrip circuit  $Z_3$ . A second microstrip circuit  $Z_2$  is interconnected the first microstrip circuit  $Z_1$  and the third microstrip circuit  $Z_3$  in a ~~meanderd~~ meandering path. As seen, the second microstrip circuit  $Z_2$  is shaped as an inverted U. Next, feed the transmission signals of high power generated by the wireless communication product to the input terminal 1 of the low pass filter. A frequency response at the output terminal 5 is shown in the graph of FIG. 4. It is seen clearly from the frequency response graph of FIG. 4 that an insertion loss is about 0.1dB, a reduction about 35.8dB at the harmonic of double frequencies of 11GHz, and a reduction about 7.17dB at the harmonic of triple frequency of 16.5GHz. These measurements are about the same as that measured in a well known filter. Particularly, the elimination of the harmonic spurious of triple frequency is not acceptable.